

LESSON PLAN

Discipline: ETC. Engg/CS & Engg.	Semester: Third (3 rd)	Name of the Lab I/C: Er Biswaranjan Nayak/Er Amit Kumar Patra
Subject: Digital Electronics. Lab	No. of days/week class allotted: Three (3)	Semester from Date: 15.09.22 to Date: 22.12.22 No. of Weeks: 15
WEEK	CLASS DAY	PRACTICAL EXPERIMENTS
1 st	1 st	Familiarization of Digital Trainer Kit Logic Pulse , logic probe and Digital IC7400,7402,7432,7486 and 7408.
	2 nd	
	3 rd	Review Class
2 nd	1 st	Verify the truth tables of AND, OR, NOT, NOR, NAND, EX OR & EX NOR GATES.
	2 nd	
	3 rd	Review Class
3 rd	1 st	Explain various gates by using universal properties of NAND gate and verify truth table.
	2 nd	Explain various gates by using universal properties of NOR gate and verify truth table.
	3 rd	Review Class
4 th	1 st	Implement half adder using logic gates.
	2 nd	Implement full adder using logic gates.
	3 rd	Review Class
5 th	1 st	Implement half subtractor using logic gates.
	2 nd	Implement full subtractor using logic gates.
	3 rd	Review Class

6 th	1 st	Study MULTIPLEXER.
	2 nd	Study DEMULTIPLEXER.
	3 rd	Review Class
7 th	1 st	Study display devices, LED.
	2 nd	Study Seven segment display.
	3 rd	Review Class
8 th	1 st	Study Shift Registers.
	2 nd	
	3 rd	Review Class
9 th	1 st	Study the Operation of Flip Flops.
	2 nd	
	3 rd	Review Class
10 th	1 st	Revision
	2 nd	
	3 rd	
11 th	1 st	Revision
	2 nd	
	3 rd	
12 th	1 st	Revision
	2 nd	
	3 rd	
13 th	1 st	Revision
	2 nd	
	3 rd	

th 14	st 1	Revision
	nd 2	
	rd 3	
th 15	st 1	Revision
	nd 2	
	rd 3	